Huawei Ascend 910B vs Huawei Ascend 910C

Side by side datacenter GPU specifications from the Flopper.io database.

OVERVIEW

	Huawei Ascend 910B	Huawei Ascend 910C
FP32 TFLOPS	—	**50.0**
Memory	**64 GB**	**128 GB**
Memory Bandwidth	**1.6 TB/s**	**3.2 TB/s**
TDP	**400 W**	**600 W**

PERFORMANCE BY PRECISION

Precision	Huawei Ascend 910B peak TFLOPS (dense)	Huawei Ascend 910C peak TFLOPS (dense)
FP64	—	—
FP32	—	50.0
TF32	—	—
BF16	400.0	800.0
FP16	400.0	800.0
FP8	—	—
FP6	—	—
FP4	—	—
INT8	800.0	1600.0

SPECIFICATIONS

	Huawei Ascend 910B	Huawei Ascend 910C
Architecture	Da Vinci v2	Da Vinci v2 (dual-die)
Process Node	SMIC 7nm	SMIC 7nm
Launch Year	2024	2025
Form Factor	OAM	OAM
Memory Type	HBM2e	HBM2e
Interface Width	—	—
Interconnect	HCCS	Unified Bus (UB)

	Huawei Ascend 910B	Huawei Ascend 910C
TDP	400 W	600 W
FP32 Efficiency	—	0.08 TFLOPS/W