



Apple M3 Ultra

Apple M3

SoC

2025

3nm

OVERVIEW

32.8 FP32 TFLOPS	512 GB VRAM LPDDR5	819 GB/s Memory Bandwidth	— TDP
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PERFORMANCE METRICS

Precision	Peak TFLOPS	Bits	Type
INT8	131.1	8	Standard
FP16	65.5	16	Standard
FP32	32.8	32	Standard

MEMORY SPECIFICATIONS

Capacity	512 GB
Type	LPDDR5
Bandwidth	819 GB/s
Interface Width	—

POWER & EFFICIENCY

TDP	—
Est. Max Power	—
FP32 Efficiency	—
FP16 Efficiency	—

HARDWARE DETAILS

Vendor: Apple	Architecture: Apple M3	Process Node: 3nm
Form Factor: SoC	Launch Year: 2025	

COMPUTE ENGINE

GPU cores: 80

CHIP DESIGN

Transistors: 184.0 billion