



Apple M5 Max

Apple M5

SoC

2025

3nm

OVERVIEW

16.6 FP32 TFLOPS	128 GB VRAM LPDDR5X	614 GB/s Memory Bandwidth	— TDP
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PERFORMANCE METRICS

Precision	Peak TFLOPS	Bits	Type
INT8	66.4	8	Standard
FP16	33.2	16	Standard
FP32	16.6	32	Standard

MEMORY SPECIFICATIONS

Capacity	128 GB
Type	LPDDR5X
Bandwidth	614 GB/s
Interface Width	—

POWER & EFFICIENCY

TDP	—
Est. Max Power	—
FP32 Efficiency	—
FP16 Efficiency	—

HARDWARE DETAILS

Vendor: Apple	Architecture: Apple M5	Process Node: 3nm
Form Factor: SoC	Launch Year: 2025	Variant: Max