



# Apple M5 Pro

Apple M5   SoC   2025   3nm

## OVERVIEW

|                           |                              |                                     |                 |
|---------------------------|------------------------------|-------------------------------------|-----------------|
| <b>8.3</b><br>FP32 TFLOPS | <b>64 GB</b><br>VRAM LPDDR5X | <b>307 GB/s</b><br>Memory Bandwidth | <b>—</b><br>TDP |
|---------------------------|------------------------------|-------------------------------------|-----------------|

## PERFORMANCE METRICS

| Precision | Peak TFLOPS | Bits | Type     |
|-----------|-------------|------|----------|
| INT8      | 33.2        | 8    | Standard |
| FP16      | 16.6        | 16   | Standard |
| FP32      | 8.3         | 32   | Standard |

## MEMORY SPECIFICATIONS

|                 |          |
|-----------------|----------|
| Capacity        | 64 GB    |
| Type            | LPDDR5X  |
| Bandwidth       | 307 GB/s |
| Interface Width | —        |

## POWER & EFFICIENCY

|                 |   |
|-----------------|---|
| TDP             | — |
| Est. Max Power  | — |
| FP32 Efficiency | — |
| FP16 Efficiency | — |

## HARDWARE DETAILS

|                  |                        |                   |
|------------------|------------------------|-------------------|
| Vendor: Apple    | Architecture: Apple M5 | Process Node: 3nm |
| Form Factor: SoC | Launch Year: 2025      | Variant: Pro      |