



Apple M5 Ultra

Apple M5 SoC 2026 3nm

OVERVIEW

33.2 FP32 TFLOPS	512 GB VRAM LPDDR5X	1.2 TB/s Memory Bandwidth	— TDP
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PERFORMANCE METRICS

Precision	Peak (dense)	Bits	Type
FP64	—	—	Standard
FP32	33.2	32	Standard
TF32	—	—	Standard
BF16	—	—	Standard
FP16	—	—	Standard
FP8	—	—	Standard
FP6	—	—	Standard
FP4	—	—	Standard
INT8	—	—	Standard

MEMORY SPECIFICATIONS

Capacity	512 GB
Type	LPDDR5X
Bandwidth	1.2 TB/s
Interface Width	—

POWER & EFFICIENCY

TDP	—
Est. Max Power	—
FP32 Efficiency	—
FP16 Efficiency	—

HARDWARE DETAILS

Vendor: Apple	Architecture: Apple M5	Process Node: 3nm
Form Factor: SoC	Launch Year: 2026	Variant: Ultra

COMPUTE ENGINE

GPU cores: 80

INTERCONNECT

GPU-to-GPU: UltraFusion

CHIP DESIGN

Chiplets: 4 (Two dual-die M5 Max
chips joined by next-generation
UltraFusion)

ENTERPRISE FEATURES

Sparsity: No