



Apple M5

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SoC

2025

3nm

OVERVIEW

4.2 FP32 TFLOPS	32 GB VRAM LPDDR5X	154 GB/s Memory Bandwidth	— TDP
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PERFORMANCE METRICS

Precision	Peak TFLOPS	Bits	Type
INT8	16.6	8	Standard
FP16	8.3	16	Standard
FP32	4.2	32	Standard

MEMORY SPECIFICATIONS

Capacity	32 GB
Type	LPDDR5X
Bandwidth	154 GB/s
Interface Width	—

POWER & EFFICIENCY

TDP	—
Est. Max Power	—
FP32 Efficiency	—
FP16 Efficiency	—

HARDWARE DETAILS

Vendor: Apple	Architecture: Apple M5	Process Node: 3nm
Form Factor: SoC	Launch Year: 2025	