



Biren BR110

Biren SPC

2024

OVERVIEW

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FP32 TFLOPS	VRAM	Memory Bandwidth	TDP

PERFORMANCE METRICS

Precision	Peak TFLOPS	Bits	Type
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MEMORY SPECIFICATIONS

Capacity	—
Type	—
Bandwidth	—
Interface Width	—

POWER & EFFICIENCY

TDP	—
Est. Max Power	—
FP32 Efficiency	—
FP16 Efficiency	—

HARDWARE DETAILS

Vendor: Biren	Architecture: Biren SPC	Process Node: —
Form Factor: —	Launch Year: 2024	