



Cambricon MLU290-M5

MLUv02 OAM 2021

OVERVIEW

— FP32 TFLOPS	— VRAM	1.2 TB/s Memory Bandwidth	350 W TDP
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PERFORMANCE METRICS

Precision	Peak TFLOPS	Bits	Type
INT8	512.0	8	Standard

MEMORY SPECIFICATIONS

Capacity	—
Type	—
Bandwidth	1.2 TB/s
Interface Width	—

POWER & EFFICIENCY

TDP	350 W
Max Power	350 W
FP32 Efficiency	—
FP16 Efficiency	—

HARDWARE DETAILS

Vendor: Cambricon	Architecture: MLUv02	Process Node: —
Form Factor: OAM	Launch Year: 2021	

COMPUTE ENGINE

MLU Core: 64

INTERCONNECT

GPU-to-GPU: MLU-Link

CHIP DESIGN

Transistors: 46.0 billion

ENTERPRISE FEATURES

ECC: Yes