

# Graphcore GC200

Colossus MK2

2020

7nm

## OVERVIEW

|             |                    |                  |     |
|-------------|--------------------|------------------|-----|
| 62.5        | —                  | —                | —   |
| FP32 TFLOPS | Memory On-die SRAM | Memory Bandwidth | TDP |

## PERFORMANCE METRICS

| Precision | Peak (dense) | Bits | Type     |
|-----------|--------------|------|----------|
| FP64      | —            | —    | Standard |
| FP32      | 62.5         | 32   | Standard |
| TF32      | —            | —    | Standard |
| BF16      | —            | —    | Standard |
| FP16      | 250.0        | 16   | Standard |
| FP8       | —            | —    | Standard |
| FP6       | —            | —    | Standard |
| FP4       | —            | —    | Standard |
| INT8      | —            | —    | Standard |

## MEMORY SPECIFICATIONS

|                 |             |
|-----------------|-------------|
| Capacity        | —           |
| Type            | On-die SRAM |
| Bandwidth       | —           |
| Interface Width | —           |

## POWER & EFFICIENCY

|                                |   |
|--------------------------------|---|
| TDP                            | — |
| Max power                      | — |
| FP32 dense TFLOPS per W of TDP | — |
| FP16 dense TFLOPS per W of TDP | — |

Per W of TDP figures divide dense peak by accelerator TDP.  
Board power only: excludes host CPUs, networking, cooling and facility overhead.

## HARDWARE DETAILS

|                   |                            |                   |
|-------------------|----------------------------|-------------------|
| Vendor: Graphcore | Architecture: Colossus MK2 | Process Node: 7nm |
| Form Factor: —    | Launch Year: 2020          |                   |

## COMPUTE ENGINE

IPU-Cores: 1,472

INTERCONNECT

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GPU-to-GPU: IPU-Link

CHIP DESIGN

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Transistors: 59.4 billion                      Die Size: 823 mm<sup>2</sup>