



Huawei Atlas 200I A2 20 TOPS

Da Vinci Module 2023

OVERVIEW

— FP32 TFLOPS	12 GB VRAM LPDDR4X	51 GB/s Memory Bandwidth	25 W TDP
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PERFORMANCE METRICS

Precision	Peak TFLOPS	Bits	Type
INT8	20.0	8	Standard
FP16	10.0	16	Standard

MEMORY SPECIFICATIONS

Capacity	12 GB
Type	LPDDR4X
Bandwidth	51 GB/s
Interface Width	—

POWER & EFFICIENCY

TDP	25 W
Est. Max Power	29 W
FP32 Efficiency	—
FP16 Efficiency	0.40 TFLOPS/W

HARDWARE DETAILS

Vendor: Huawei	Architecture: Da Vinci	Process Node: —
Form Factor: Module	Launch Year: 2023	Variant: 20 TOPS

COMPUTE ENGINE

Da Vinci AI Core: 1	Boost Clock: 1.22 GHz
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