

Meta MTIA 100

MTIA

2023

7nm

OVERVIEW

— FP32 TFLOPS	64 GB Memory LPDDR5	176 GB/s Memory Bandwidth	25 W TDP
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PERFORMANCE METRICS

Precision	Peak (dense)	Bits	Type
FP64	—	—	Standard
FP32	—	32	Standard
TF32	—	—	Standard
BF16	51.2	16	Standard
FP16	51.2	16	Standard
FP8	—	—	Standard
FP6	—	—	Standard
FP4	—	—	Standard
INT8	102.4	8	Standard

MEMORY SPECIFICATIONS

Capacity	64 GB
Type	LPDDR5
Bandwidth	176 GB/s
Interface Width	—

POWER & EFFICIENCY

TDP	25 W
Max power (Flopper estimate)	~29 W est.
FP32 dense TFLOPS per W of TDP	—
FP16 dense TFLOPS per W of TDP	2.05 TFLOPS/W
Flopper estimate: 25 W TDP x 1.15. The vendor publishes no maximum board power for this part. Per W of TDP figures divide dense peak by accelerator TDP. Board power only; excludes host CPUs, networking, cooling and facility overhead.	

HARDWARE DETAILS

Vendor: Meta	Architecture: MTIA	Process Node: 7nm
Form Factor: —	Launch Year: 2023	

COMPUTE ENGINE

INTERCONNECT

PCIe: 4.0 x8

CHIP DESIGN

Die Size: 373 mm²

Chiptlets: 1 (Monolithic)