

SambaNova SN50

RDU

Custom

2026

5nm

OVERVIEW

—	64 GB	—	—
FP32 TFLOPS	Memory HBM2E	Memory Bandwidth	TDP

PERFORMANCE METRICS

Precision	Peak (dense)	Bits	Type
FP64	—	—	Standard
FP32	—	—	Standard
TF32	—	—	Standard
BF16	1600.0	16	Standard
FP16	—	—	Standard
FP8	3200.0	8	Standard
FP6	—	—	Standard
FP4	—	—	Standard
INT8	—	—	Standard

MEMORY SPECIFICATIONS

Capacity	64 GB
Type	HBM2E
Bandwidth	—
Interface Width	—

POWER & EFFICIENCY

TDP	—
Max power	—
FP32 dense TFLOPS per W of TDP	—
FP16 dense TFLOPS per W of TDP	—
Per W of TDP figures divide dense peak by accelerator TDP. Board power only: excludes host CPUs, networking, cooling and facility overhead.	

HARDWARE DETAILS

Vendor: SambaNova	Architecture: RDU	Process Node: 5nm
Form Factor: Custom	Launch Year: 2026	