



Lenovo ThinkSystem SR685a V3

8x Instinct MI300X CDNA 3 rack

OVERVIEW

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Peak Dense	Aggregate Memory	Aggregate Bandwidth	Aggregate Power

CONFIGURATION

Accelerator	Instinct MI300X 192GB
Count	8
Memory per GPU	192 GB
Bandwidth per GPU	5.3 TB/s

POWER & EFFICIENCY

Aggregate Power	—
TDP per GPU	750 W
Efficiency	—
Aggregate Bandwidth	—

SYSTEM DETAILS

Vendor: Lenovo	Family: ThinkSystem	Architecture: CDNA 3
Form Factor: rack	Release Year: —	Process Node: 5nm FinFET

INTERCONNECT

Eight AMD Instinct MI300X accelerators on a single 750 W 8-GPU board, with eight front PCIe slots connected to a switch for GPU connectivity

NOTES

Lenovo's 8U AMD accelerator server, and the machine Lenovo builds around the Instinct MI300X. Two AMD EPYC processors, either 5th generation 9005 Turin or 4th generation 9004 Genoa, reach 128 cores at up to 400 W and are paired with up to 3 TB of host DDR5 across 24 DIMMs. The accelerators arrive as one factory-integrated 750 W eight-GPU board rather than as separate modules, ordered under feature code C1HK. Power is eight hot-swap supplies in full N+N redundancy, all standard rather than optional, and the chassis measures 447 by 351 by 924 mm at up to 109.4 kg. Lenovo publishes no system power draw, no accelerator memory total and no system-level throughput; its guide gives per-GPU figures only. Worth noting that Lenovo's earlier datasheet for this model number describes an HGX H200 machine, while the current product guide describes this MI300X configuration; the guide is the source used here.