



NVIDIA Groq 3 LPX

256x Groq 3 LPU Groq LPU rack 2026

OVERVIEW

307 FP8 PFLOPS Dense	— Aggregate Memory	— Aggregate Bandwidth	— Aggregate Power
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PERFORMANCE METRICS

Precision	Peak (PFLOPS)	Per GPU (TFLOPS)	TFLOPS/W
FP8	307.2	1200.0	—

All figures are dense. Vendors commonly headline the sparse number, which is twice the dense one.

CONFIGURATION

Accelerator	Groq 3 LPU
Count	256
Memory per GPU	—
Bandwidth per GPU	—

POWER & EFFICIENCY

Aggregate Power	—
TDP per GPU	—
FP8 Efficiency	—
Aggregate Bandwidth	—

SYSTEM DETAILS

Vendor: NVIDIA	Family: LPX	Architecture: Groq LPU
Form Factor: rack	Release Year: 2026	Process Node: —

INTERCONNECT

LPU C2C (chip-to-chip) / C2C spine